

IN THE SPECIFICATION

Please replace paragraph [0056] with the following amended paragraph.

B1 [0056] Moreover, if the redundancy register stores data pertinent to an entire column of redundant cells, and the entire column of redundant cells is split between a plurality of redundant planes, it follows that one redundancy register may contain data relevant to more than one redundant plane. Thus, in the exemplary circuitry of FIG. 9A, there is a first redundancy register 71 configured to store a redundant column address that may be relevant to at least one cell in redundant plane 43 and at least one cell in redundant plane [[44]] 45.